ABSTRACT OF THE DISCLOSURE

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A chip resistor including an elongated chip substrate, a resistive layer formed on the substrate, a silver-containing upper electrode connected to the resistive layer, an undercoat enclosing the resistive layer and extending onto part of the upper electrode, an auxiliary electrode connected to the upper electrode and extending onto part of the undercoat, and overcoat enclosing the undercoat and extending onto part of the auxiliary electrode. In the longitudinal direction of the substrate The undercoat extends longitudinally of the substrate beyond the overcoat, so that the extremity of the undercoat is offset from the extremity of the overcoat by an appropriate distance.

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